

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

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Title of Invention	BRANCH INSTRUCTION FOR PROCESSOR ARCHITECTURE
Application Number :	10/069229
Confirmation Number:	1429
First Named Applicant:	Gilbert Wolrich
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Examiner:	
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US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No	Pub. No.	Date	Applicant	Kind	Class	Subclass
	1	20020038403	2002-03-28	Wolrich et al.			

Signature

Examiner Name	Date